



Atty. Dkt. No. 016887-1104

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Noboru NODA

Title: SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

Appl. No.: 10/813,446

Filing Date: 03/31/2004

Examiner: S. Crane

Art Unit: 2811

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §1.56**

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of documents known to  
Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR §1.56.

A copy of each non-U.S. patent document and each non-patent document is being  
submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not  
intended as an admission that such document constitutes prior art against the claims of the  
present application or that such document is considered material to patentability as defined in  
37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be  
appropriate to antedate or otherwise remove as a competent reference any document which is  
determined to be a *prima facie* art reference against the claims of the present application.

**TIMING OF THE DISCLOSURE**

The listed documents are being submitted in compliance with 37 CFR §1.97(b), before the mailing date of the first Office Action on the merits.

**RELEVANCE OF EACH DOCUMENT**

In a counterpart Japanese application, a Japanese Office Action issued on May 6, 2005. According to an English translation obtained by Applicants' representative of the Japanese Office Action, the Japanese Patent Office made certain characterizations of the references. A copy of the English translation of the Japanese Office Action is attached.

The disclosure of these characterizations should not be construed as an admission of or agreement to the opinions expressed in the Japanese Office Action.

Document A4 discloses the formation of pin holes of a semiconductor device and the manufacture of the semiconductor device.

Document A6 discloses the manufacture of a semiconductor device.

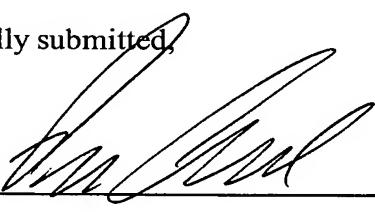
Unless otherwise indicated, no English translation is readily available (not considering machine-generated translations that may be freely available online, to both the Applicant and the PTO) for each of the non-English language documents. However, a commercially available English language abstract is provided herewith, where indicated on the attached Form PTO/SB/08. Inasmuch as Applicants have endeavored to provide at least one item that complies with the requirement for a "concise explanation of relevance" for each of the non-English language documents, each of these documents has been submitted in compliance with the PTO requirements and should be considered by the Examiner (37 CFR §1.97, §1.98 and MPEP §609).

Applicant respectfully requests that each listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

By \_\_\_\_\_

  
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Date July 6, 2005

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Substitute for form 1449B/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Date Submitted: July 6, 2005  (use as many sheets as necessary)				<b>Application Number</b> 10/813,446 <b>Filing Date</b> 03/31/2004 <b>First Named Inventor</b> Noboru NODA <b>Group Art Unit</b> 2811 <b>Examiner Name</b> Unassigned	
Sheet	1	of	1	Attorney Docket Number	016887-1104

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	A1	JP	03-022440	A	NEC CORP.	01-30-1991		Abst.
	A2	JP	2001-068479	A	MATSUSHITA ELECTRIC IND. CO., LTD.	03-16-2001		Abst.
	A3	JP	2002-270819	A	ALPS ELECTRIC CO., LTD.	09-20-2002		Abst.
	A4	JP	61-042138	A	HITACHI LTD., ET AL.	02-28-1986		Abst.
	A5	JP	63-193562	A	TOSHIBA CORP.	08-10-1988		Abst.
	A6	JP	60-235464	A	NEC CORP., ET AL.	11-22-1985		Abst.

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

**Burden Hour Statement:** This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.